

Features:

- ♦ Supports 1.0 to 11.3Gb/s bit rates
- ♦ Hot-Pluggable
- ♦ Duplex LC connector

- ♦ MMF links up to 220m
- ♦ Power Supply: +3.3V
- ♦ Power consumption<1W
- → Temperature Range: 0~ 70°C
- ♦ RoHS compliant

Applications:

- ♦ 10GBASE-LRM Ethernet
- ♦ Legacy FDDI multimode links
- **♦** SDH/SONET Aplication
- ♦ 10G Fibre Channel

Description:

The KW3902D is a very compact 10Gb/s optical transceiver module for serial optical communication applications at 10Gb/s. The KW3902D converts a 10Gb/s serial electrical data stream to 10Gb/s optical output signal and a 10Gb/s optical input signal to 10Gb/s serial electrical data streams. The high speed 10Gb/s electrical interface is fully compliant with SFI specification. The high performance 1310nm FP transmitter and high sensitivity PIN receiver provide superior performance for Ethernet applications at up to 220m links on MMF.

The SFP+ Module compliants with SFF-8431, SFF-8432 and IEEE 802.3aq 10GBASE-LRM. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472. The fully SFP compliant form factor provides hot pluggability, easy optical port upgrades and low EMI emission.

• Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	T_{S}	-40		+85	°C
Case Operating Temperature	TA	0		70	°C
Maximum Supply Voltage	Vcc	-0.5		4	V
Relative Humidity	RH	0		85	%

■ Electrical Characteristics (Top = 0 to 70 °C, VCC = 3.0 to 3.60 Volts)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	Vcc	3.135		3.465	V	
Supply Current	Icc			300	mA	
Power Consumption	P			1	W	
Transmitter Section:						
Input differential impedance	Rin		100		Ω	1
Tx Input Single Ended DC Voltage Tolerance (Ref VeeT)	V	-0.3		4	V	
Differential input voltage swing	Vin,pp	180		700	mV	2
Transmit Disable Voltage	V_{D}	2		Vcc	V	3
Transmit Enable Voltage	V_{EN}	Vee		Vee+0.8	V	
Receiver Section:						
Single Ended Output Voltage Tolerance	V	-0.3		4	V	
Rx Output Diff Voltage	Vo	180		850	mV	
Rx Output Rise and Fall Time	Tr/Tf	30			ps	4
LOS Fault	V _{LOS fault}	2		Vcc _{HOST}	V	5
LOS Normal	V _{LOS}	Vee		Vee+0.8	V	5

Note:

- 1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
- 2. Per SFF-8431 Rev 3.0
- 3. Into 100 ohms differential termination.
- 4. $20\% \sim 80\%$
- 5. LOS is an open collector output. Should be pulled up with $4.7k 10k\Omega$ on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.

● Optical Parameters(Top = 0 to 70°C, Vcc = 3.00 to 3.60 Volts)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note			
Transmitter Section:									
Center Wavelength	λt	1260	1310	1355	nm				
RMS spectral width	$\lambda_{ m RMS}$			2	nm				
Average Optical Power	Pavg	-7.3		-1	dBm	1			
Optical Power OMA	Poma	-4.5		+1.5	dBm				
Laser Off Power	Poff			-30	dBm				
Extinction Ratio	ER	3.5			dB				
Transmitter Dispersion Penalty	TDP			4.7	dB	2			
Relative Intensity Noise	Rin			-128	dB/Hz	3			
Optical Return Loss Tolerance		20			dB				
Receiver Section:									
Center Wavelength	λr	1260	1310	1620	nm				
Receiver Sensitivity	Sen			-11.1	dBm	4			

10G SFP+ Transceiver 220m (KW3902D) Hot Pluggable, Duplex LC, +3.3V 1310nm FP-LD

Stressed Sensitivity (OMA)	Sen _{ST}		-6.5	dBm	4
Los Assert	LOSA	-30	-	dBm	
Los De-assert	LOS_D		-12	dBm	
Los Hysteresis	LOS _H	0.5		dB	
Overload	Sat	0		dBm	5
Receiver Reflectance	Rrx		-12	dB	

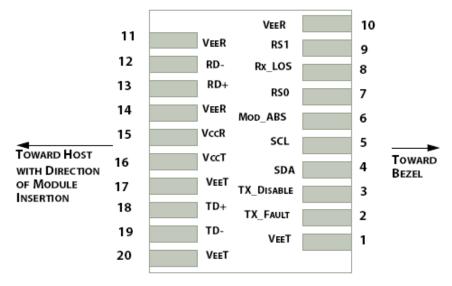
Note

- 1. Average power figures are informative only, per IEEE802.3aq.
- 2. TWDP figure requires the host board to be SFF-8431compliant. TWDP is calculated using the Matlab code provided in clause 68.6.6.2 of IEEE802.3aq.
- 3. 12dB reflection.
- 4. Conditions of stressed receiver tests per IEEE802.3aq. CSRS testing requires the host board to be SFF-8431 compliant.
- 5. Receiver overload specified in OMA and under the worst comprehensive stressed condition.

• General Specificatio

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Bit Rate	BR	1.0		11.3	Gb/s	
Bit Error Ratio	BER			10-12		
Maximum Supported Distances						
Fiber Type	850nm OFL Bandwidth					
62.5μm	500MHz-km			220	m	
50um	400 MHz-km			100	m	
50μm	OM2 500 MHz-km			220	m	
G.652	-			300	m	

Pin Assignment



Module Interface to Host

Pin Function Definitions

PIN#	Name	Function	Notes
1	VeeT	Module transmitter ground	1
2	Tx Fault	Module transmitter fault	2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	2
7	RS0	Rate Select 0. Not Used	
8	LOS	Receiver Loss of Signal Indication	4
9	RS1	Rate Select 1. Not Used	
10	VeeR	Module receiver ground	1
11	VeeR	Module receiver ground	1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	
20	VeeT	Module transmitter ground	1

Notes:

- 1. The module ground pins shall be isolated from the module case.
- 2. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.
- 3. This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.
- 4. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

• SFP Module EEPROM Information and Management

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP -8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I²C interface at address A0h and A2h.

The memory is mapped in Table 1.

Detailed ID information (A0h) is listed in Table 2.

And the DDM specification at address A2h.

For more details of the memory map and byte definitions, please refer to the SFF-8472, "Digital Diagnostic Monitoring Interface for Optical Transceivers". The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

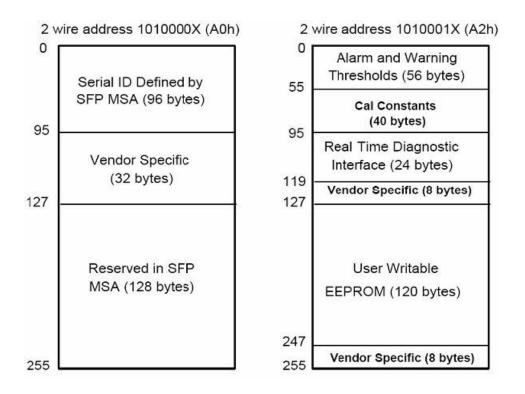


Table 2 - EEPROM Serial ID Memory Contents (A0h)

Data Address	Length (Byte)	Name of Length	Description and Contents
Base ID Fields	, y ,		
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	10G Base-LRM
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: Kewei fiber
36	1	Reserved	
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID
40-55	16	Vendor PN	Part Number: "KW3902D" (ASCII)
56-59	4	Vendor rev	Revision level for part number
60-62	3	Reserved	
63	1	CCID	Least significant byte of sum of data in address 0-62

Extended ID Fi	elds		
64-65	2	Option	Indicates which optical SFP signals are implemented (001Ah = LOS, TX_FAULT, TX_DISABLE all supported)
66	1	BR, max	Upper bit rate margin, units of %
67	1	BR, min	Lower bit rate margin, units of %
68-83	16	Vendor SN	Serial number (ASCII)
84-91	8	Date code	Kewei fiber's Manufacturing date code
92-94	3	Reserved	
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)
Vendor Specific	ID Fields	3	
96-127	32	Readable	Kewei fiber specific date, read only
128-255	128	Reserved	Reserved for SFF-8079

• Digital Diagnostic Monitor Characteristics

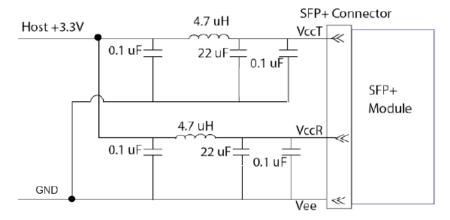
Data Address	Parameter	Accuracy	Unit
96-97	Transceiver Internal Temperature	±3.0	°C
98-99	VCC3 Internal Supply Voltage	±5.0	%
100-101	Laser Bias Current	±10	%
102-103	Tx Output Power	±3.0	dBm
104-105	Rx Input Power	±3.0	dBm

• Regulatory Compliance

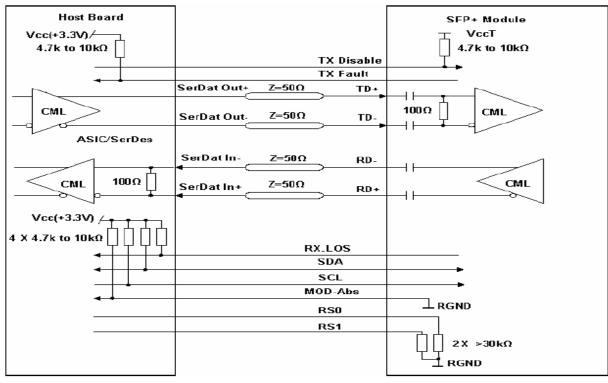
The OP3902D complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge	MIL-STD-883E	Class 1(>1000 V)
(ESD) to the Electrical Pins	Method 3015.7	
Electrostatic Discharge	IEC 61000-4-2	Compatible with standards
(ESD)	GR-1089-CORE	
to the Duplex LC Receptacle		
Electromagnetic	FCC Part 15 Class B	Compatible with standards
Interference (EMI)	EN55022 Class B (CISPR 22B)	
	VCCI Class B	
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11	Compatible with Class 1 laser
	EN60950, EN (IEC) 60825-1,2	product.

• Recommended Circuit

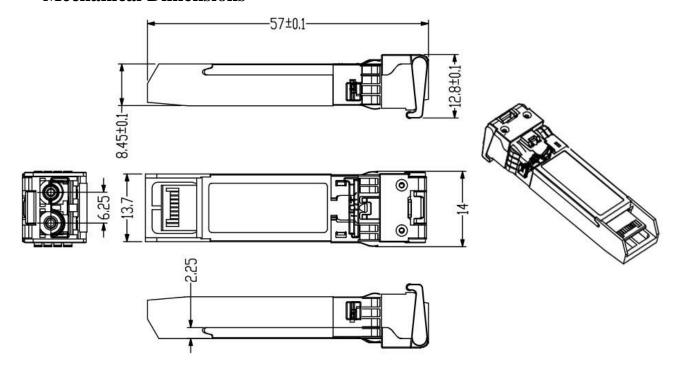


Recommended Host Board Power Supply Circuit



Recommended High-speed Interface Circuit

• Mechanical Dimensions



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